

# Message from the Editor-in-Chief

HIROYUKI TOMIYAMA<sup>1,a)</sup>

On behalf of the editorial board, I am pleased to present the eighth volume of the IPSJ Transactions on System LSI Design Methodology (TSLDM). Similar to previous volumes, the eighth volume is going to contain February and August issues. Each issue, in turn, is going to contain a couple of invited papers written by world-famous researchers in addition to peer-reviewed papers.

Our daily life relies heavily on Information and Communication Technology (ICT), and of course, advanced ICT is enabled by Systems-on-Chip (SoCs). TSLDM is a unique and premier journal focusing on SoC design methodology. TSLDM covers various aspects of SoC design methodology, including design experience, architecture, synthesis, modeling, simulation, verification, testing, embedded software, emerging applications, and so on.

TSLDM was first launched in 2008, and it has now established its prestigious status in this research community. This is thanks to a large number of people such as the authors who submitted high-quality papers to the journal, the anonymous reviewers who carefully reviewed the submissions and returned constructive comments to the authors, and the editorial board members who edited the journal. I would like to express my sincere appreciation to all of them.

The SoC design methodology continues evolving. TSLDM also has to continue evolving. To this end, I will step down as Editor-in-Chief (EiC) of TSLDM, and Professor Nozomu Togawa has been appointed to take my place. I am sure that he will further enhance the value of TSLDM.

Last, but not least, I would like to encourage researchers in this field to submit their latest achievements to TSLDM. Definitely, TSLDM continues to be an ideal journal because of its open-access policy, timely publication, and established reputation.

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<sup>1</sup> Ritsumeikan University, Kusatsu, Shiga 525-8577, Japan

<sup>a)</sup> ht@fc.ritsumeiki.ac.jp