

# APL Interactive Processing System on a Minicomputer

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## Abstract

This paper deals with the presentation of an APL interpreter implemented on a minicomputer (HITAC-10). It includes a syntax analyzer which translates the external APL language to an intermediate language. The analysis phase consists of two steps in order to distinguish unary operators from binary operators and so on. First it scans from left to right and then from right to left. Moreover, during execution of all APL statements, dynamic storage allocation is used. Since there are insufficient data storage in the main memory, this system makes use of different kinds of data management techniques.

We will describe the processing method of the APL interpreter and show the design concept used to make APL match a minicomputer's characteristic.

### 1. Introduction

APL (A Programming Language) [1] was first defined by K.E. Iverson as a machine-independent programming language whose descriptive and analytic powers were adequate to concisely describe algorithms. There are features which distinguish APL from the more traditional programming language such as FORTRAN [2]. Some of its features include; (1) it allows a clear and simple representation of the sequence in which steps of the expression are evaluated; (2) it provides a concise and mathematical notation for the operations occurring in a wide range of processes; (3) it permits the description of a process to be independent of the choice of a particular representation of the data.

We were interested in actually making use of a programming language with these features and have implemented such a processing system on a minicomputer. From the point of view of the implementation, we aimed at making APL match a minicomputer's characteristic.

### 2. Outline of Implementation

#### *Processing Method*

This system takes advantage of an interpreter approach, with a consequent loss of execution speed. The source language is translated into an intermediate language which we call the step-sequences, and then the interpreter continuously evaluates it. Fig.1 shows the data flow on this system. Here the processing data divides the processing path into two modes: execution and definition. In execution mode, this system produces the intermediate language and then evaluates it; and in definition mode, this system only preserves it in system file without evaluation.

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This paper first appeared in Japanese in Joho-Shori (Journal of the Information Processing Society of Japan), Vol. 16, No. 9 (1975), pp. 781~788.

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With the absence of type declarations and DIMENSION statements, an attribute of the variables can't be decided until values are actually assigned to the variables during execution. Accordingly, we can't adopt the use of a compiler to generate efficient codes. But, when defined functions are called, this system evaluates the step-sequences preserved in definition mode because it is not very effective to evaluate the source pattern directly.

*System Construction*

The hardware construction consists of HITAC-10(12kW), Magnetic Drum(131kW) and a typewriter with an APL typing-element.

The number of the program steps are more than 20,000 words; 1 word is 16 bits. Also, this system deals with many areas and tables. Therefore, an overlay method is required. The memory map in Fig.2 shows two phases. Namely, as the processing procedure is composed of a translation part and an interpretation part, the overlay structure of this program is also constructed to make system performance more effective. We designed with a view in: (1) making system performance more effective; (2) making this system match a minicomputer's characteristic; (3) making the program structure clear.

3. Translation into Intermediate Language

*Translation Process*

The syntax of APL has characteristics as follows; (1) a statement is evaluated sequentially from right to left, but an expression in parentheses is looked on as an operand; (2) there are not priorities among many primitives; (3) many primitives operate not only as unary operators but also as binary ones; (4) there are neither type declarations nor DIMENSION statements; (5) whether or not a statement is an output one depends on its sentential form.

The analyzer has to scan from right to left because the evaluation is from right

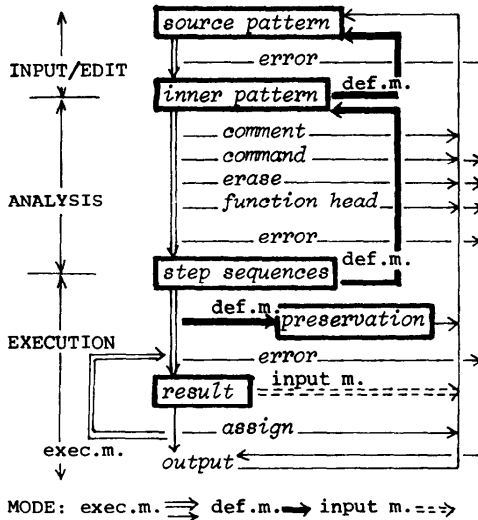


Fig.1 Flow of Processing Data

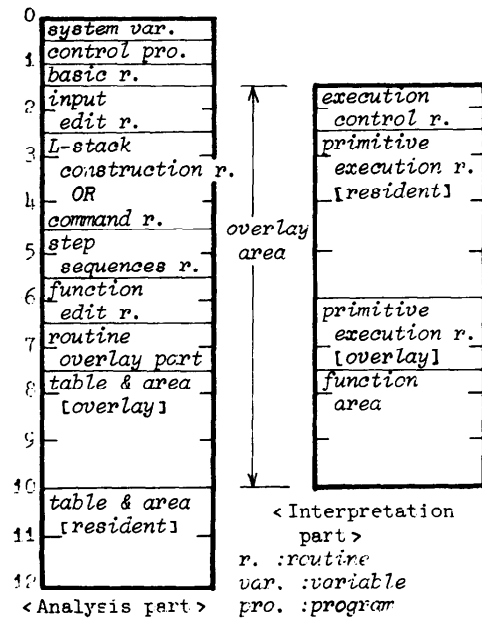


Fig.2 Memory Map





we divided it by itself into two kinds of areas: function data area and global data area. As constants used in defined functions are required only on evaluation, the system loads them to a fixed data area on the function call. This not only facilitates to edit constants of defined functions, but also gives merits to make use of the data

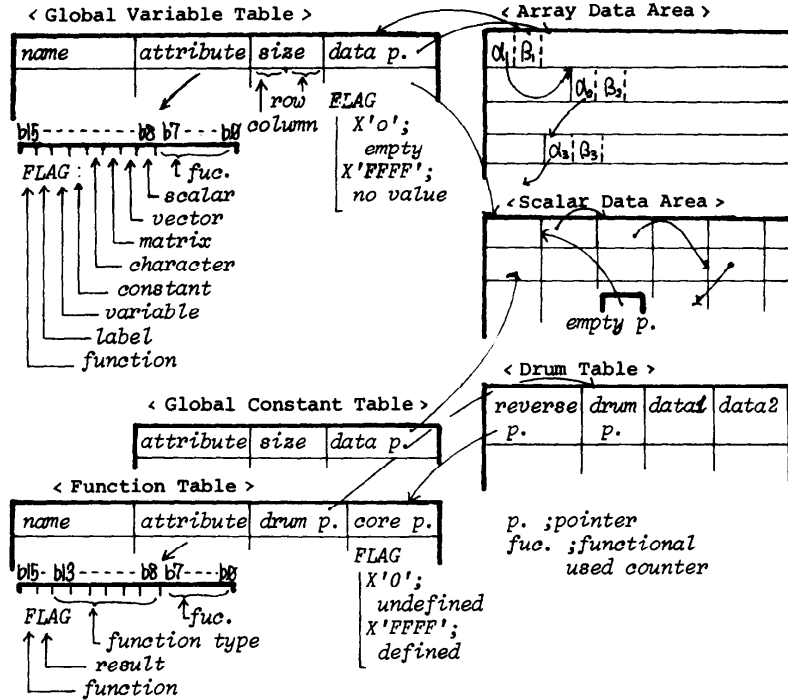


Fig.4 Data Entries

area. On the other hand, the global data area is furthermore divided into the scalar area and the array area. This is because we assume that the usage of a scalar is large even though APL is excellent enough to deal with arrays. In the scalar area, the data cell is 2 words; all empty cells are composed of a list structure. Also, the array area is a list structure of variable-length data cell which is attended with 2-word information cell. This information cell links each data cell in spite of whether or not each data cell is empty. One word is a pointer of the list structure (e.g.  $\alpha_1, \alpha_2, \alpha_3$  in Fig.4), and the other one is a flag which indicates whether or not the cell is empty (e.g.  $\beta_1, \beta_2, \beta_3$  in Fig.4). In the case of being empty, it points that the cell is empty (unused); otherwise, it is a reverse pointer to an entry table. A garbage collector first reconstructs the cells using reverse pointers. Second, it rewrites the data pointers. And finally it produces more data areas.

5. Conclusion

There are several problems we must consider. On the defined function, this system adopted the method such that the step-sequences were evaluated on the function call after preserved in the form of the intermediate language in order to make system performance more effective. This led to the problem of the transition of the attribute between functions and variables. But the problem must be investigated from the point of view of both effectiveness and dynamic features. In the future, new ideas will allow to make system performance effective[3,4].

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