

# Message from the Editor-in-Chief

HIROYUKI TOMIYAMA<sup>1,a)</sup>

On behalf of the editorial board, I am pleased to present the seventh volume of the IPSJ Transactions on System LSI Design Methodology (TSLDM). Similar to previous volumes, the seventh volume is going to contain February and August issues. Each issue, in turn, is going to contain a couple of invited papers written by world-famous researchers in addition to peer-reviewed regular papers.

TSLDM is going to retain its unique features which distinguish TSLDM from other journals in our research community. TSLDM provides an open-access online publication so that authors can more efficiently disseminate their important achievements to the widest R&D community of the world. Papers can be browsed and downloaded worldwide through the Internet free of charge. Another important feature of TSLDM is its timely and rapid publication. We do not have a backlog problem. Every issue has a pre-determined editorial schedule announced in the call for papers, so that authors can expect the dates of notification and publication. This makes it more attractive to submit and publish up-to-date information on research and development.

We hope that TSLDM offers an ideal place for both researchers who want to publish their achievements and those who want to learn state-of-the-art technologies. Latest call for papers and submission instructions can be found on our website. We look forward to receiving your contributions.

Finally, but not least, I would like to thank the associate editors-in-chief and the associate editors who have been working hard to edit the seventh volume of TSLDM.

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