IPSJ Transactions on Advanced Computing Systems Vol. 4 No. 4 1–11 (Oct. 2011)

*Regular Paper*

# **Retrospective Study of Performance and Power Consumption of Computer Systems**

## HISANOBU TOMARI<sup>†1</sup> and KEI HIRAKI<sup>†1</sup>

Power consumption has become an important factor in the design of highperformance computer systems. The power consumption of newer systems is now published but is unknown for many older systems. Data for only two or three generations of systems are insufficient for projecting the performance/power of future systems. We measured the performance and power consumption of 70 computer systems from 1989 to 2011. Our collection of computers included desktop and laptop personal computers, workstations, handheld devices and supercomputers. This is the first paper reporting the performance and power consumption of systems over twenty years, using a uniform method. The primary benchmark we used was Dhrystone. We also used NAS Parallel Benchmarks and CPU2006 suite. The Dhrystone/power ratio was found to be growing exponentially. The data we obtained indicates that the Dhrystone result and the CINT2006 in SPEC CPU2006 correlate closely. The NAS Parallel Benchmarks and CFP2006 results also correlate. Using the trend of Dhrystone/power that we obtained, we predict that the Dhrystone/power ratio will reach 2,963 VAX MIPS/Watt in 2018, when exaflops machines are expected to appear.

#### **1. Introduction**

To predict the performance and power consumption of future systems, it is important to study that of past and present systems. The performance of computer systems was measured using benchmark software that was popular around the time the computer was manufactured. We can compare the performance of systems in the same generation using published benchmark results. However, because the popular benchmark software changes over time, it is difficult to compare systems across generations. In recent years, power consumption has become an important factor in computing. The power consumption of older systems was not measured because it was not considered critical in the design of computer systems until recently.

We examined 70 computer systems that were manufactured in the years 1989 to 2011 and that include handheld devices, workstations, and a vector supercomputer. We used Dhrystone<sup>31</sup>, NAS Parallel Benchmarks<sup>2)</sup> (NPB) and CPU2006 16) benchmarks. The power consumption of the systems was measured using electrical testers. This is the first paper to report the power consumption of as many as 70 computers spanning the course of 20 years.

Cooper, Bell, Lin and Rasmussen benchmarked four microprocessors using exactly the same circuitry outside the processor  $s$ ). Our focus is on system performance and system power consumption, rather than those of processor alone. Bailey, Barszcz, Dagum and Simon measured the result of NPB on supercomputers at NASA Ames Research Center in 1993 3). Our list includes a more recent and a wider range of systems. The power consumption of recent systems has been published using SPECpower benchmarks 26). However, the published results only include systems marketed recently. As the workload of the SPECpower benchmark runs on a Java virtual machine, it cannot measure the power consumption of systems where Java is not available (e.g., Human68K). Moreover, the optimization levels of Java virtual machine largely depends on the architecture where it runs. We used Dhrystone to measure the power consumption. A comparison of performance/watt on three generations of Google servers has been published  $5$ . The systems that we tested span many more generations than the servers at Google do. It has been observed that the older versions of SPEC and Dhrystone show similar results 20). By running them on many configurations, both old and new we found this to be true for latest version of the SPEC benchmark.

We found that the power consumption of desktop and workstation systems has not changed as much as the performance. We also found a close correlation among the results of Dhrystone, NPB and SPEC CPU2006. Finally, we have forecasted the performance-per-watt in the exaflop era.

#### **2. Materials and Methods**

#### **2.1 Computers**

We examined computers that were available in years from 1989 to 2011. The year of a computer is defined as the year when the configuration of the com-

<sup>†</sup>1 The University of Tokyo

puter system was made possible. For example, NEC PC-9801RA system was available in 1988 but was upgraded with a Cyrix Cx486DLC processor that was not available until 1992. Hence, the year of the system is 1992. The exact year of availability of systems or components was unclear for some computers so we estimated the year using advertisements in magazine archives.

The processors we benchmarked include Motorola/Freescale 68000 30), 68030, MPC7447A, MPC7450, i.MX515, IBM PPC601 6), PPC750 23), POWER5 21), Cell BE<sup>7</sup>, HP PA-7100LC<sup>25</sup>), MIPS R4000<sup>29</sup>, R5000, R12000<sup>15</sup>, DEC EV45<sup>27</sup>), EV56 4), EV67 24), Sun microSPARC, microSPARC II, UltraSPARC II 14), Ultra-SPARC III 18), Intel 80286, 80386, i486 9), Pentium 1), Pentium III, Pentium D, Core 2<sup>12)</sup>, Atom, Core i7, Itanium 2<sup>28)</sup>, AMD Am5x86, K6-III<sup>11)</sup>, K7<sup>13)</sup>, K8<sup>22)</sup>, K10 10), Renesas SH-4A, Cyrix Cx486DLC, VIA C3, NEC SX-9, Marvell Feroceon and NVIDIA Tegra 2. Detailed information on the system configurations is available in the Appendix.

#### **2.2 Measuring Power Consumption**

The power consumption was measured with a Fluke 336 clamp meter, a Sanwa Supply TAP-TST7 tester or a Metaprotocol UbiWattMeter. The Fluke 336 clamp meter is rated at 2% precision for the voltages we measured. The Sanwa Supply TAP-TST7 is rated at 0.2% and 0.3% precision for the voltage and current. The electrical testers were connected to the AC input of the computer systems.

We measured the power consumption at two states in each system. The first state is the idle state, where the power consumption of the system stabilizes after the computer is turned on. The other state is the running state, where the system is running the Dhrystone benchmark. On laptop systems, the display backlight was turned off during this experiment.

## **2.3 Performance Benchmarks**

We used several benchmark software suites to evaluate the performance of each system. The first benchmark is Dhrystone version 2.1 in C language. This benchmark runs on systems with a smaller amount of memory. On most systems, Dhrystone runs inside the cache memory 32). Therefore, the resulting measurements of power consumption are based on that of the processor core alone, and the power that is required to communicate with memory chips outside the processor is not measured. A DEC VAX 11/780 is supposed to perform at 1,757 runs/s. We normalized our Dhrystone results to that performance to get *VAX MIPS* equivalent performance metrics.

NPB is a collection of numerical benchmark programs. We used version 3.3.1 to estimate the floating-point performance of the systems. On all systems, we consistently used size A. We found that around 512 MB of memory is required for this problem size in order to obtain any useful results. The NPB figures we used for comparison are geometric means of normalized results (*NPB base ratio*) of individual benchmarks to the results on the Sun Ultra60 (UltraSPARC-II 360 MHz).

The last benchmark suite we used is SPEC CPU2006. These benchmarks share the workload kernel with real applications, and have a larger memory footprint than Dhrystone. CPU2006 requires 1,024 megabytes on 32-bit pointer machines 17). The large memory footprint prevents the CPU2006 from running on older machines, so our CPU2006 results are limited to machines where sufficient amount of memory was available. The rules for running CPU2006 are defined by SPEC, which we followed on most of the systems. However, on NEC SX-9, we used 'specinvoke' to directly run each benchmark in order to use the job queue on the system.

#### **3. Results**

#### **3.1 Dhrystone and Power Consumption**

The Dhrystone benchmark confirmed that the processor performance is still increasing over the years (**Fig. 1**). Because Dhrystone runs inside the cache memory on most processors with caches, this improvement is due to the improvement in processor cores and not the supporting circuitry like memory controllers and caches.

The power consumption of mainstream systems is slightly higher on newer systems than on older ones (**Fig. 2**). Larger SMP systems with power consumption higher than 400W are not plotted. Power consumption of NEC SX-9 is an estimate using one fourth of the power consumption of another SX-9 with 16 processors. We could not measure the power consumption of some machines in the method we used because they operate on batteries or they stopped working during this experiment. The power consumption in the idle state and in the



running state changed little on most of the older systems, whereas on the newer systems it changed by dozens of watts. This reflects the power-saving features available on these new designs. As our electrical tester was attached to the AC input of the computer systems, the power consumption includes that of hard drives, graphic controllers, chipsets and other peripheral devices. For example, the SPARCstation 5 with a 85 MHz microSPARC II consumed 5 watts more power than its 110 MHz counterpart. This is attributed to the power consumed by different hard drive models. Even though this makes comparing the result harder, it is useful because it represents the power that a computer system con-



**Fig. 2** Power consumption; the error bar represents idle state and running state.

sumes when it is configured as a cluster node or accelerator host. In some older systems, the power consumption in the running state was lower than that in the idle state by one to four watts. We are investigating this issue.

Performance per power consumption is also increasing (**Fig. 3**), but this is driven mainly by performance improvements. Even though the distribution is similar to that of Fig. 1, the high-performance system tends to score low in the performance/power metric. As Dhrystone is a single-threaded benchmark, large SMP systems like Sun Fire 3800 with four threads and IBM p5 570 with 32 threads perform badly in this metric. Multi-core systems would have scored better if we used multithreaded benchmark programs, but newer designs that feature multicore usually also support power-saving features, so the resulting performance/power ratio will not grow as high as the number of processor cores. The highest performance/power ratio is achieved by an Atom N270 (1,600 MHz) netbook with the Intel Compiler Suite 11.1, at 468.36 VAX MIPS/W followed by other portable machines. However, it is important to note that the Atom netbook performed at less than half the performance with GCC 4.5.1 compiler (4,683 VAX MIPS vs. 2,152 VAX MIPS). We will discuss the compiler issues later. Other portables also scored better in this metric.

The trend line on Fig. 3 is calculated using the least square method. As the trend is changed in year 1995, the fitting is based on data in years 1995 to 2011. In year *y*, the approximate VAX MIPS/Watt is calculated as:



4 Retrospective Study of Performance and Power Consumption of Computer Systems

**Fig. 3** Dhrystone/power; Dhrystone performance divided by the power consumption in running state. Not all systems shown in Fig. 1 appear in this figure.

$$
dw = \exp(0.31(y - 1988) - 1.35)
$$
\n(1)

Using TOP500 projection, it is estimated we will get exaflops systems in about 2018. Using this equation, we can estimate that in the year 2018, the Dhrystone/power ratio of desktop processors will be approximately 2,963 VAX MIPS/Watt if this trend continues. For example, a system with an Intel Atom N475 at 1,833 MHz performs at 2,960 VAX MIPS and its whole system consumes twelve watts of power, so we are going to increase the performance/power to twelve times its current level.

Dividing Dhrystone by the processor frequency yields a performance/cycle ratio



**Fig. 4** Dhrystone/clock.

(**Fig. 4**). The performance/cycle ratio is largely dependent on the microarchitecture of each processor. The performance/cycle ratio of embedded processors is also improving at a similar rate to those of contemporary desktop and server processors. The high-performance systems are often high performance/cycle systems. However, high-performance/power systems have lower performance/cycle than high-performance systems do. It remains to be seen whether the performance/cycle of high-performance/power systems will also stall for eight years as happened with desktop systems.

The performance of the NEC SX-9 supercomputer was lower than expected on the Dhrystone benchmark, because Dhrystone is hard to vectorize. Numerical



**Fig. 5** NPB OpenMP results on Intel Core i7-860@2800 MHz (4 cores/8 threads)/ICC, AMD Phenom 9350e@2000 MHz (4C/4T)/GCC and NEC SX-9 (4C/4T)/SXCC; G: Geometric mean, H: Harmonic mean.

applications such as in NPB, that are not optimized for vector supercomputers, can often be vectorized and run faster than conventional processors like Intel Core i7 (**Fig. 5**). We used OpenMP implementation of the NPB <sup>19)</sup>. This characteristic is true for both NPB and CFP2006. The SX-9 performed the best among the systems we tested, in geometric mean metric for all of these floating-point benchmarks. It is expected that for specially-optimized programs the SX-9 will perform even better.

#### **3.2 Relations between Benchmarks**

We ran three benchmarks on many computer configurations and the characteristics of these benchmarks are now compared. Not all systems that we tested have a sufficiently large memory space to run CPU2006 or NPB. We ran benchmarks on all machines that satisfied minimum memory requirement for each benchmark. The Dhrystone and the CINT2006 results correlates well (**Fig. 6**). The correlation coefficient is 0.986. Even though it is often considered obsolete, Dhrystone still reflects system performance as well as CINT2006. There were two cases where a machines deviate from the main trend. One case is where Dhrystone performs better than expected from CINT2006 scores, and the other case is where Dhrystone performs worse than CINT2006. Both cases are caused by the dependency of Dhrystone performance on the string functions in the standard C library. Intel compiler links objects against highly optimized string functions



**Fig. 7** CFP2006 and NPB base ratio geometric mean.

shipped with the compiler. The performance of string functions in GNU C library differs from version to version, but generally the newer the better. Using the same string manipulation functions will increase the precision of Dhrystone benchmark. The inter-procedure optimization (IPO) in the Intel Compiler Suite also does an excellent job of optimizing Dhrystone whereas IPO is not available in GCC.

NPB and CFP2006 also correlate well (**Fig. 7**). These NPB figures are based on the serial implementation of the benchmark. The correlation coefficient of the geometric mean of the NPB ratio and CFP2006 is 0.878. In the case of SX-9, the

performance of a particular program depends almost solely on how much part of the program can be vectorized. Excluding NEC SX-9 raises the figure to 0.979.

#### **4. Conclusion**

We measured the power consumption of old and new systems. First, we found that improvement in the performance/power ratio was driven mainly by performance improvements. The embedded processors like the Intel Atom and the ARM have a better performance/power ratio, but still lack the performance to use them in high-performance computers. Secondly, performance/power evaluations revealed that performance/power ratio will improve to only 10 times that of current processors in 2018, when we are scheduled to deliver exaflops systems. Finally, we showed that there are strong correlations between the SPEC CPU2006 benchmarks, the NPB and the Dhrystone. Even though the SPEC CPU2006 is popular as the standard for evaluating system performance, it is large and hard to run in experimental or prototype setups. We showed that the SPEC CPU2006 can be substituted by Dhrystone and NPB in cases where total system performance is to be measured.

Even though further analysis of performance on more specific features of processors requires more benchmarks using computers with similar configurations, running the same benchmark on many different configurations was useful in obtaining an overview of the improvement in system performance. We want to include POWER7 and SPARC64-VIIIfx systems to our list as soon as they became available for our benchmarking. Newer systems should be benchmarked as they emerge to understand where we are and to improve system performance.

Acknowledgments Benchmarks were in part carried out on the NEC SX-9 at Center for Computational Astrophysics, CfCA, of the National Astronomical Observatory of Japan. We thank them for letting us use their system for benchmarking. We are also grateful to Takeshi Watanabe for contributing three computers for this experiment, and Kazuei Hironaka for driving his car to help us transport the computers.

#### **References**

- 1) Alpert, D. and Avnon, D.: Architecture of the Pentium microprocessor, *Micro, IEEE*, Vol.13, No.3, pp.11–21 (online), DOI:10.1109/40.216745 (1993).
- 2) Bailey, D.H., Barszcz, E., Barton, J.T., Browning, D.S., Carter, R.L., Dagum, L., Fatoohi, R.A., Frederickson, P.O., Lasinski, T.A., Schreiber, R.S., Simon, H.D., Venkatakrishnan, V. and Weeratunga, S.K.: The NAS parallel benchmarks, *SC Conference*, Vol.0, pp.158–165 (online),

DOI:http://doi.ieeecomputersociety.org/10.1145/125826.125925 (1991).

- 3) Bailey, D., Barszcz, E., Dagum, L. and Simon, H.: NAS parallel benchmark results, *Parallel Distributed Technology: Systems Applications, IEEE*, Vol.1, No.1, pp.43–51 (online), DOI:10.1109/88.219861 (1993).
- 4) Bannon, P. and Keller, J.: Internal architecture of Alpha 21164 microprocessor, *Compcon '95. 'Technologies for the Information Superhighway', Digest of Papers*, pp.79–87 (online), DOI:10.1109/CMPCON.1995.512368 (1995).
- 5) Barroso, L.A.: The Price of Performance, *Queue*, Vol.3, pp.48–53 (online), DOI:http://doi.acm.org/10.1145/1095408.1095420 (2005).
- 6) Becker, M., Allen, M., Moore, C., Muhich, J. and Tuttle, D.: The Power PC 601 microprocessor, *Micro, IEEE*, Vol.13, No.5, pp.54–68 (online), DOI:10.1109/40.238002 (1993).
- 7) Chen, T., Raghavan, R., Dale, J.N. and Iwata, E.: Cell Broadband Engine Architecture and its first implementation–A performance view, *IBM J. Res. Dev.*, Vol.51, No.5, pp.559–572 (online), DOI:10.1147/rd.515.0559 (2007).
- 8) Cooper, T., Bell, W., Lin, F. and Rasmussen, N.: A Benchmark Comparison of 32-bit Microprocessors, *Micro, IEEE*, Vol.6, No.4, pp.53–58 (online), DOI:10.1109/MM.1986.304780 (1986).
- 9) Crawford, J.: The i486 CPU: Executing instructions in one clock cycle, *Micro, IEEE*, Vol.10, No.1, pp.27–36 (online), DOI:10.1109/40.46766 (1990).
- 10) Dorsey, J., Searles, S., Ciraula, M., Johnson, S., Bujanos, N., Wu, D., Braganza, M., Meyers, S., Fang, E. and Kumar, R.: An Integrated Quad-Core Opteron Processor, *Solid-State Circuits Conference, 2007, ISSCC 2007, Digest of Technical Papers, IEEE International*, pp.102–103 (online), DOI:10.1109/ISSCC.2007.373608 (2007).
- 11) Draper, D., Crowley, M., Holst, J., Favor, G., Schoy, A., Ben-Meir, A., Trull, J., Khanna, R., Wendell, D., Krishna, R., Nolan, J., Partovi, H., Johnson, M., Lee, T., Mallick, D., Frydel, G., Vuong, A., Yu, S., Maley, R. and Kauffmann, B.: An X86 microprocessor with multimedia extensions, *Solid-State Circuits Conference, 1997, Digest of Technical Papers, 43rd ISSCC, 1997 IEEE International*, pp.172–173, 450 (online), DOI:10.1109/ISSCC.1997.585321 (1997).
- 12) George, V., Jahagirdar, S., Tong, C., Ken, S., Damaraju, S., Scott, S., Naydenov, V., Khondker, T., Sarkar, S. and Singhj, P.: Penryn: 45-nm next generation Intel Core 2 processor, *Solid-State Circuits Conference, 2007, ASSCC '07, IEEE Asian*,

pp.14–17 (online), DOI:10.1109/ASSCC.2007.4425784 (2007).

- 13) Golden, M., Hesley, S., Scherer, A., Crowley, M., Johnson, S., Meier, S., Meyer, D., Moench, J., Oberman, S., Partovi, H., Weber, F., White, S., Wood, T. and Yong, J.: A seventh-generation x86 microprocessor, *IEEE Journal of Solid-State Circuits*, Vol.34, No.11, pp.1466–1477 (online), DOI:10.1109/4.799851 (1999).
- 14) Goldman, G. and Tirumalai, P.: UltraSPARC-II: the advancement of ultracomputing, *Compcon '96, 'Technologies for the Information Superhighway' Digest of Papers*, pp.417–423 (online), DOI:10.1109/CMPCON.1996.501804 (1996).
- 15) Gwennap, L.: MIPS R12000 to Hit 300 MHz, *Microprocessor Report*, Vol.11, No.13 (1997).
- 16) Henning, J.L.: SPEC CPU2006 benchmark descriptions, *SIGARCH Comput. Archit. News*, Vol.34, pp.1–17 (online),

DOI:http://doi.acm.org/10.1145/1186736.1186737 (2006).

- 17) Henning, J.L.: SPEC CPU2006 memory footprint, *SIGARCH Comput. Archit. News*, Vol.35, pp.84–89 (online), DOI:http://doi.acm.org/10.1145/1241601.1241618 (2007).
- 18) Horel, T. and Lauterbach, G.: UltraSPARC-III: designing third-generation 64-bit performance, *Micro, IEEE*, Vol.19, No.3, pp.73–85 (online), DOI:10.1109/40.768506 (1999).
- 19) Jin, H., Frumkin, M. and Yan, J.: The OpenMP Implementation of NAS Parallel Benchmarks and its Performance, Technical report (1999).
- 20) Kainaga, M., Yamada, K. and Inayoshi, H.: Analysis of SPEC benchmark programs, *Proc. 8th TRON Project Symposium, 1991*, pp.208–215 (online), DOI:10.1109/TRON.1991.213102 (1991).
- 21) Kalla, R., Sinharoy, B. and Tendler, J.: IBM POWER5 chip: A dualcore multithreaded processor, *Micro, IEEE*, Vol.24, No.2, pp.40–47 (online), DOI:10.1109/MM.2004.1289290 (2004).
- 22) Keltcher, C., McGrath, K., Ahmed, A. and Conway, P.: The AMD Opteron processor for multiprocessor servers, *Micro, IEEE*, Vol.23, No.2, pp.66–76 (online), DOI:10.1109/MM.2003.1196116 (2003).
- 23) Kennedy, A., Alexander, M., Fiene, E., Lyon, J., Kuttanna, B., Patel, R., Pham, M., Putrino, M., Croxton, C., Litch, S. and Burgess, B.: A G3 PowerPC superscalar low-power microprocessor, *Proc. Compcon '97, IEEE*, pp.315–324 (online), DOI:10.1109/CMPCON.1997.584742 (1997).
- 24) Kessler, R., McLellan, E. and Webb, D.: The Alpha 21264 microprocessor architecture, *Proc. International Conference on Computer Design: VLSI in Computers and Processors, 1998, ICCD '98*, pp.90–95 (online), DOI:10.1109/ICCD.1998.727028 (1998).
- 25) Knebel, P., Arnold, B., Bass, M., Kever, W., Lamb, J., Lee, R., Perez, P., Undy, S. and Walker, W.: HP's PA7100LC: A low-cost superscalar PA-RISC processor, *Compcon Spring '93, Digest of Papers*, pp.441–447 (online),

DOI:10.1109/CMPCON.1993.289711 (1993).

- 26) Lange, K.-D.: Identifying Shades of Green: The SPECpower Benchmarks, *Computer*, Vol.42, No.3, pp.95–97 (online), DOI:10.1109/MC.2009.84 (2009).
- 27) McLellan, E.: The Alpha AXP architecture and 21064 processor, *Micro, IEEE*, Vol.13, No.3, pp.36–47 (online), DOI:10.1109/40.216747 (1993).
- 28) McNairy, C. and Soltis, D.: Itanium 2 processor microarchitecture, *Micro, IEEE*, Vol.23, No.2, pp.44–55 (online), DOI:10.1109/MM.2003.1196114 (2003).
- 29) Mirapuri, S., Woodacre, M. and Vasseghi, N.: The Mips R4000 processor, *Micro, IEEE*, Vol.12, No.2, pp.10–22 (online), DOI:10.1109/40.127580 (1992).
- 30) Stritter, E. and Gunter, T.: A Microprocessor Architecture for a Changing World: The Motorola 68000, *Computer*, Vol.12, No.2, pp.43–52 (online), DOI:10.1109/MC.1979.1658617 (1979).
- 31) Weicker, R.P.: Dhrystone benchmark: Rationale for version 2 and measurement rules, *SIGPLAN Not.*, Vol.23, pp.49–62 (online), DOI:http://doi.acm.org/10.1145/47907.47911 (1988).
- 32) Weicker, R.P.: Dhrystone: A synthetic systems programming benchmark, *Commun. ACM*, Vol.27, pp.1013–1030 (online), DOI:http://doi.acm.org/10.1145/358274.358283 (1984).

### **Appendix**

Hardware configurations and power consumption are shown in **Table 1**. 'C' represents the number of processor cores in the system. Versions of the operating system, compilers, and performance results are on **Table 2**.

<sup>7</sup> Retrospective Study of Performance and Power Consumption of Computer Systems

8 Retrospective Study of Performance and Power Consumption of Computer Systems

Machine	<b>CPU</b>	MHz	С	Mem MB	Year	$\mathbf{P}_{\mathrm{idle}}[\mathbf{W}]$	$P_{run}[W]$
SHARP X68000 PRO HD	MC68000	10	$\mathbf{1}$	$\overline{2}$	1989	38	38
SONY NWS-1460	MC68030, 68882	25	$1\,$	16	1989	53	57
Apple Macintosh IIci	MC68030, 68882	25	$\mathbf{1}$	32	1989	34	38
EPSON PC-286UX	286, 287XL	12	$\mathbf{1}$	3	1990	38	38
Sun SparcStation IPX	microSPARC	40	$\mathbf{1}$	64	1991	35	36
NEC PC-9801DA	i386DX, i387DX	20	$\mathbf{1}$	5	1991	48	48
NEC PC-9801RA	Cx486DLC, FasMath	20	$\mathbf{1}$	12	1992	60	60
Fujitsu FM TOWNS II HR	i486SX	20	$\mathbf{1}$	10	1992	48	48
SGI IRIS Indigo R4000	R <sub>4000</sub>	100	$\mathbf{1}$	320	1992	107	109
EPSON PRO-486	i486DX2	66	$\mathbf{1}$	13	1993	78	78
NEC PC-9821As2	i486SX	33	$\mathbf{1}$	36	1993	59	59
<b>NEC PC-9801BS2</b>	i486SX	33	$\mathbf{1}$	$\overline{4}$	1993	22	20
HP 9000 712/80	<b>PA-7100LC</b>	80	$\mathbf{1}$	32	1994	47	47
Sun SPARCstation 5/85	microSPARC II	85	$\mathbf{1}$	96	1994	49	52
Sun SPARCstation 5/110	microSPARC II	110	$\mathbf{1}$	32	1994	44	47
Apple PowerMac 7100/80	PPC601	80	$\mathbf{1}$	136	1995	64	64
Advantech PCA-6144V	Am5x86-P75	133	$\mathbf{1}$	16	1996		
NEC PC-9821V13	Pentium ODP	167	$\mathbf{1}$	64	1996	42	54
SGI <sub>O2</sub>	R5000	200	$\mathbf{1}$	256	1996	67	74
Sun Ultra2 $2200\,$	UltraSPARC	200	$\overline{2}$	512	1996	165	166
DEC AlphaStation 255/300	EV45	300	$\mathbf{1}$	256	1996	95	91
DEC AlphaStation 500/400	EV56	400	$\mathbf{1}$	256	1996	102	104
PalmPilot Professional	MC68328	16	$\mathbf{1}$	$\mathbf{1}$	1997	$\frac{1}{2}$	
Sun Ultra5	US-IIi	270	$\mathbf{1}$	512	1998	56	56
Sun Ultra60 2360	US-II	360	$\overline{2}$	1,152	1998	$\overline{\phantom{0}}$	$\overline{\phantom{a}}$
Symbol SPT 1500	MC68328	16	$\mathbf{1}$	$\overline{2}$	1998	$\overline{\phantom{a}}$	$\overline{\phantom{a}}$
<b>SGI VWS 320</b>	Pentium II	400	$\overline{2}$	256	1999	123	124
Intergraph TDZ 2000 GX1	P3 Xeon	550	$\overline{2}$	1,024	1999	94	122
Sun Ultra60 1450	US-II	450	$\mathbf{1}$	1,280	1999	130	130
Compaq XP1000	EV67	667	$\mathbf{1}$	1,536	1999	215	214
API UP2000	EV67	750	$\overline{2}$	2,048	1999	289	289
Apple PowerBook G3(Pismo)	<b>PPC750</b>	400	$\mathbf{1}$	512	2000	12	17
SGI Octane2	R12000	400	$\overline{2}$	1,024	2000	260	258
Shuttle FV25	Tualatin	1,133	$\mathbf{1}$	768	2001	51	55
Apple PowerMac G4 (DA)	MPC7450	800	$\mathbf{1}$	768	2001	110	113
Sun Fire 3800	US-IIIcu	900	$\overline{4}$	23,552	2001	1,318	1,318
Cobalt Qube 3 Plus	$K6-2+$	450	$\mathbf{1}$	512	2002	22	36
Sun Blade 2000	US-IIIcu	900	$\mathbf{1}$	8,192	2002	195	194
Tyan Tiger MPX	Athlon MP	1,666	$\sqrt{2}$	2,048	2002	178	199
Palm m130		33	$\mathbf{1}$	8	2002	3.7	3.7
Apple PowerMac G4 (FW800)	MC68VZ328 <b>MPC7455</b>	1,250	$\,2\,$	2,048	2003	164	171
Apple PowerMac $G5(7,2)$	PPC970		$\boldsymbol{2}$			124	262
		2,000	$\mathbf{1}$	3,072	2003		
Palm Zire 71	OMAP 310	144		16	2003	6.3	6.5
VIA EPIA-ML	Nehemiah	800	$\mathbf{1}$	512	2004	36	44
IBM p5 570	POWER5	1,900	16	32,768	2004	2,772	2,814

**Table 1** Hardware configurations and power consumption.

Apple PowerBook G4	<b>MPC7447A</b>	1,666	$\mathbf{1}$	2,048	2004	18	38
Intel SR870BH2	Itanium2	1,400	$\overline{2}$	4,096	2004	329	357
HP Integrity rx5670	Itanium2	1,300	$\overline{4}$	24,576	2004	663	688
Sun Fire V40z	Opteron 850	2,400	$\overline{4}$	7,680	2004		
HP ProLiant DL145 G2	Opteron 275	2,200	$\overline{2}$	2,048	2005	139	151
Leadtek Winfast K8N	Sempron $2600+$	1,600	$\mathbf{1}$	2,048	2005	95	117
Sony Playstation 3	Cell BE	3,200	$\mathbf{1}$	256	2006		
ASUS P5LD2 SE	Pentium D	3,000	$\boldsymbol{2}$	3,072	2006	103	135
Toshiba Dynabook CX/47E	Core 2 Duo	2,000	$\overline{2}$	2,048	2007	16	36
XFX nForce 780i	Core 2 Quad	2,666	$\overline{4}$	8,192	2007	120	142
SH-2007	SH4A	400	$\mathbf{1}$	128	2007		9
QNAP TS-409	MV88F5281-D0	500	$\mathbf{1}$	256	2008	26	26
DELL Inspiron 910	Atom N270	1,600	$\mathbf 1$	1,024	2008	$\overline{7}$	10
NEC SX-9	$SX-9$	3,200	$\overline{4}$	131,072	2008	$\qquad \qquad -$	7,240
J&W MINIX-780G-SP128M	Phenom X4 9350e	2,000	$\overline{4}$	3,072	2008	73	88
Convey HC-1	Xeon 5138	2,133	$\overline{2}$	24,576	2008		
Buffalo Kuro-box/T4	<b>MPC8241</b>	266	$\mathbf{1}$	128	2009	21	22
SHARP PC-Z1	i.MX515	800	$\mathbf{1}$	512	2009	2.2	4.1
DELL PowerEdge R410	Xeon E5530	2,400	8	12,288	2009	116	148
ASUS P7P55D LE	Core i7 860	2,800	$\overline{4}$	2,048	2009	82	123
Intel S5520HCR	Xeon 5680	3,333	6	6,144	2010	107	137
Fujitsu Lifebook MH380/1A	Atom N475	1,833	$\mathbf{1}$	1,024	2010	9	12
Toshiba Dynabook AZ	Tegra 2	1,000	$\overline{2}$	512	2010		
Lenovo ThinkPad X201s	Core $i7640LM$	2,133	$\overline{2}$	8,192	2010	15	33
ASRock P67 Extreme6	Core i7 2600K	3,400	$\overline{4}$	4,096	2011	48	68

**Table 2** Software configurations and performance results.





11 Retrospective Study of Performance and Power Consumption of Computer Systems

ASUS P7P55D LE	Linux 2.6.18	Intel $11.1$	23.547.94	19.71	35.73	32.44
Intel $S5520HCR$	Linux 2.6.35	Intel $11.1$	24,033.47	22.94	37.95	34.27
Fujitsu Lifebook MH380/1A	Linux 2.6.35	GCC 4.5.1	2.960.31	2.56	$\overline{\phantom{a}}$	
Toshiba Dynabook AZ	Linux 2.6.29	GCC 4.4.5	1.828.43	$\overline{\phantom{a}}$		
Lenovo ThinkPad X201s	Linux 2.6.35.6	GCC 4.5.1	13.917.89	14.32	$\overline{\phantom{a}}$	
ASRock P67 Extreme6	Linux 2.6.32	GCC 4.5.2	19.883.04	27.28	34.99	33.34

(Received January 28, 2011) (Accepted June 17, 2011)



**Hisanobu Tomari** received his B.S. degree from the University of Tokyo in 2010. He is currently pursuing his Master's degree in the University of Tokyo. His interests are in computer system architecture and high performance computing. He welcomes your donation of old computers.



**Kei Hiraki** graduated from the Department of Physics at the University of Tokyo, received his M.S. in physics from the Graduate School of Science, the University of Tokyo on 1978, Ph.D. in physics from the Graduate School of Science, the University of Tokyo on 1982. Then he joined Electrotechnical Laboratory from 1982 to 1991. Professor Hiraki spent two years at IBM T.J. Watson Research Center as a visiting scientist from 1988 to 1990.

Then he moved to the Faculty of Science at the University of Tokyo. He is now a professor of the Department of Creative Informatics, the Graduate School of Information Science and Technology, the University of Tokyo. Professor Hiraki has been working in both very high-speed computing and very high-speed internet systems. In the field of very high-speed computing systems, he developed several research computers such as FLATS system for algebraic computation, SIGMA-1 for dataflow computation, GRAPE-DR system, which is ranked No.1 at June 2010 Green 500 supercomputer list. As for very high-speed Internet system, his group developed basic technology to utilize very long distance, very high-speed network, and built a data sharing system called Data Reservoir. His group currently holds both the IPv4 and IPv6 Internet2 Land Speed Records.