コヒーレントノイズ下の表面符号における フォールトトレラント量子誤り訂正の サンプリングに基づいたシミュレーション

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概要:本研究では、コヒーレントノイズ下でのフォールトトレラント量子誤り訂正のサンプリングに基づ いたシミュレーション方法を提案する。このシミュレーションでは、過回転に起因するコヒーレントノイ ズとインコヒーレントノイズの混合ノイズを擬確率分布により Clifford チャンネルに分解する。そして、 適切な後処理を施し Clifford チャンネルをサンプルすることで、論理エラー率の不偏推定量を構成する。 また、チャンネルロバストネスによりサンプリングコストを評価することを通じて、フルベクトルシミュ レーションでは困難な比較的大きな符号距離を持つ平面表面符号ですら、提案したサンプリングベースの 手法が実行可能であることを示した。デモンストレーションとして、符号距離が5で81 量子ビットから なる平面表面符号のノイズのある反復的なシンドローム測定をシミュレーションした。その結果、コヒー レントエラーにより論理エラーが増加することを示した。これは、擬確率分布シミュレーションを意味の ある課題に実用化したものであり、近未来の量子デバイス上で実験的に量子誤り訂正を研究する上で有用 である。

Sampling-based quasi-probability simulation for fault-tolerant quantum error correction on the surface codes under coherent noise

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Abstract: We propose a sampling-based simulation for fault-tolerant quantum error correction under coherent noise. A mixture of incoherent and coherent noise, possibly due to over-rotation, is decomposed into Clifford channels with a quasiprobability distribution. Then an unbiased estimator of the logical error probability is constructed by sampling Clifford channels with appropriate post-processing. We characterize the sampling cost via the channel robustness and find that the proposed method is feasible even for planar surface codes with relatively large code distances intractable for full state-vector simulations. We simulate repetitive faulty syndrome measurements on the planar surface code of distance 5 with 81 qubits as a demonstration. We find that the coherent error increases the logical error rate. This is a practical application of the quasiprobability simulation for a meaningful task and would be useful to explore experimental quantum error correction on the near-term quantum devices.

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1. Introduction

Quantum error correction (QEC) is an essential ingredient for developing scalable fault-tolerant quantum computers because quantum information is vulnerable to environmental noise [1], [2]. QEC counteracts noise by encoding quantum information into a subspace of multiple qubits, which assures computation with arbitrary precision in quantum computers. Massive experimental efforts have been devoted to demonstrating small-scale QEC circuits as testbeds toward large-scale QEC circuits in the future as well as numerical simulations [3], [4], [5]. It is thus important to investigate performances of QEC codes theoretically to establish a plausible goal for experiments.

Most numerical studies for QEC have been conducted by assuming stochastic Pauli noise to exploit the efficient simulatability of stabilizer states [6], [7]. Specifically, Ref. [8] numerically calculated the threshold error rate of the rotated surface code under single- and two-qubit depolarizing channels with circuit-level noise and observed a threshold error rate of 0.57%. This result suggests that the surface code can cope with the error rate that current state-of-the-art quantum computers are reaching [5], [9]. While the computational overhead increases when compared to the Pauli noise, we can also efficiently simulate the Clifford noise such as stochastic Clifford gates and Pauli projections.

In practice, however, quantum devices often suffer from noise that cannot be described by Clifford operations. A major type of such noise is coherent unitary noise which is caused by the miscalibration of quantum gates which leads to over- or under-rotations. Ref. [10] has developed a method to detect over-rotation errors using randomized benchmarking and detected $\pi/128$ over- or under-rotation errors in their superconducting qubit. While the error has been calibrated subsequently in Ref. [10], one can expect that a small amount of such errors beyond the experimental sensitivity are still present.

Analysis of the performance of QEC in such realistic situations still remains a challenge. QEC circuits under non-Clifford noise have been investigated either by brute-force simulations [11], [12] or by exploiting exact solvability of free fermion dynamics [13], [14]. However, full state-vector simulations require exponential computational resource with respect to the code distance and are currently limited to distance-3 surface code which uses 25 qubits [11]. While the use of approximate simulation using tensor network [12] has pushed the limit to 153 qubits with perfect syndrome measurements, it is still difficult to scale up the simulation. On the other hand, free fermion simulations can handle coherent errors in a scalable manner. However, their usage is limited to certain cases: onedimensional repetition codes with faulty syndrome measurements [13] which can only correct X errors and surface codes with perfect syndrome measurements [14].

In this work, we propose a sampling-based simulation method widely applicable for fault-tolerant QEC circuits under a mixture of coherent and incoherent noise with multiple rounds of faulty syndrome measurements. The central idea is to decompose (possibly non-Clifford) noise channels into the sum of completely stabilizer preserving (CSP) channels [15]. We simulate the circuits by sampling CSP channels according to quasi-probability distributions, which is obtained from the decompositions [15], [16]. Each realization is efficiently simulable since the simulation of CSP channels involves only stabilizer states. Note that Bennink et al. have conducted similar simulations for small systems such as Steane's 7-qubit code [16]. We significantly improve the computational cost required for the simulation by providing more efficient decomposition of noise channels than Ref. [16]. This reveals that we can perform an efficient simulation in the presence of coherent errors without any additional overhead for a wide range of practically interesting parameter region. Furthermore, even outside this region, the proposed quasiprobability method enables us to simulate a surface code of distance 5 with 81 qubits on a single workstation within a reasonable computational time.

As demonstrations, we simulate the planar surface code under the code capacity coherent noise with distance up to d = 7 and under the phenomenological coherent noise with distance up to d = 5. The result shows that such non-Clifford noise deteriorates the logical error rate as expected. We also evaluated how many samples are required to simulate the logical error rate reliably as a function of the noise parameters and the code distance. This reveals that the proposed method allows us to simulate the planar surface code with relatively large code distances, which are intractable for full state-vector simulations, with a reasonable computational overhead. The proposed method provides a benchmark for building small-scale fault-tolerant quantum computers in the NISQ era.

2. Simulation of QEC circuits under coherent noise

In this section, we discuss how to calculate a logical er-

ror rate of a QEC code by simulating quantum circuits with a quasi-probability sampling of CSP channels. QEC requires two types of qubits: data qubits, which constitute logical qubits, and measurement qubits, which are used for detecting errors on data qubits. The measurements extract eigenvalues of code stabilizers by measuring the latter, and these eigenvalues are called error syndromes. For a distance d code, we repeat such measurements for d rounds. We use a (noisy) Clifford circuit $\mathcal{E}_{\text{synd}}$ for the repetitive syndrome measurements. The measurement qubits of different rounds are to be treated as different qubits to simplify the notation. Let b be the error syndrome in space and time. When data qubits are initialized to $|0_L\rangle$, the probability of obtaining a specific error syndrome b is given by

$$p(b) = \left\langle b \middle| \operatorname{Tr}_{\text{data}} \left[\mathcal{E}_{\text{synd}} \left(\left| 0_L \right\rangle \left\langle 0_L \right| \otimes \left| 0^{|b|} \right\rangle \left\langle 0^{|b|} \right| \right) \right] \middle| b \right\rangle \quad (1)$$

where $|b\rangle$ and $|0^{|b|}\rangle$ are final and initial states of the measurement qubits. After the extraction of error syndrome, we feed b to decoding algorithms such as a minimumweight perfect-matching algorithm to find a possible recovery operation \mathcal{R}_b which corrects errors on data qubits.

To express the logical error probability p_L in a closed form, we define the following infidelity function:

$$F(\rho) \coloneqq 1 - \langle 0_L | \operatorname{Tr}_{\text{meas}}[\rho] | 0_L \rangle.$$
 (2)

Using Eqs. (1) and (2), p_L can be written as,

$$p_L = \sum_b F \left(\mathcal{R}_b \circ \mathcal{P}_b \circ \mathcal{E}_{\text{synd}} \left(|0_L \rangle \langle 0_L| \otimes |0^{|b|} \rangle \langle 0^{|b|} | \right) \right),$$
(3)

where \mathcal{P}_b is the projection onto $|b\rangle\langle b|$. If the noise introduced in \mathcal{E}_{synd} is a stochastic Pauli or Clifford error, one can simulate \mathcal{E}_{synd} efficiently and can estimate the logical error rate p_L . However, efficient simulatability vanishes if noise involves non-Clifford channels.

We now describe an idea to deal with more general noise by a quasi-probability method [15], [16], [17], [18], [19], [20], [21], [22]. \mathcal{E}_{synd} can be decomposed into (noisy) elementary operations as $\mathcal{E}_{synd} = \mathcal{E}^{(L)} \circ \cdots \circ \mathcal{E}^{(1)}$. Here L is the total number of quantum operations in \mathcal{E}_{synd} . $\mathcal{E}^{(i)}$ can be decomposed over CSP and completely postive trace-preserving (CPTP) channels $\mathcal{S}_{k}^{(i)}$ in terms of a quasi-probability distribution $c_{k}^{(i)}$ as,

$$\mathcal{E}^{(i)} = \sum_k c_k^{(i)} \mathcal{S}_k^{(i)}$$

This decomposition can alternatively be written as,

$$\mathcal{E}^{(i)} = \sum_{k} p_k^{(i)} R_* \left(\mathcal{E}^{(i)} \right) \operatorname{sign} \left(c_k^{(i)} \right) \mathcal{S}_k^{(i)},$$

where

$$R_*(\mathcal{E}) \coloneqq \min_{\{c_k\}} \left\{ \sum_k |c_k|; \mathcal{E} = \sum_k c_k \mathcal{S}_k \right\},$$
$$p_k^{(i)} \coloneqq \frac{\left|c_k^{(i)}\right|}{R_*(\mathcal{E}^{(i)})}.$$

 $R_*(\mathcal{E})$ is called channel robustness [15], the square of which characterizes the sampling cost, as will be seen later. Using this decomposition for each $\mathcal{E}^{(i)}$, $\mathcal{E}_{\text{synd}}$ becomes,

$$\mathcal{E}_{\text{synd}} = \sum_{\vec{k}} p_{\vec{k}} R_{\text{*tot}} \lambda_{\vec{k}} \mathcal{S}_{\vec{k}}, \qquad (4)$$

where the summation is taken over all possible $\vec{k} = (k^{(1)}, k^{(2)}, \dots, k^{(L)})$, and

$$\begin{aligned} |\mathbf{0}\rangle\langle\mathbf{0}| &\coloneqq |0_L\rangle\langle 0_L| \otimes |0^{|b|}\rangle\langle 0^{|b|} |\\ p_{\vec{k}} &\coloneqq \prod_{i=1}^L p_{k^{(i)}}^{(i)}, \\ \lambda_{\vec{k}} &\coloneqq \prod_{i=1}^L \operatorname{sign}\left(c_{k^{(i)}}^{(i)}\right), \\ \mathcal{S}_{\vec{k}} &\coloneqq \mathcal{S}_{k^{(L)}}^{(L)} \circ \cdots \circ \mathcal{S}_{k^{(1)}}^{(1)}, \\ R_{*\operatorname{tot}} &\coloneqq \prod_{i=1}^L R_*\left(\mathcal{E}^{(i)}\right). \end{aligned}$$

Finally, combining Eqs. (3) and (4) we conclude,

$$p_L = \sum_b \sum_{\vec{k}} p_{\vec{k}} R_{*\text{tot}} \lambda_{\vec{k}} F \big(\mathcal{R}_b \circ \mathcal{P}_b \circ \mathcal{S}_{\vec{k}} (|\mathbf{0}\rangle\langle\mathbf{0}|) \big).$$

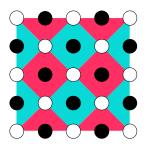
This implies that, when \vec{k} is sampled from $p_{\vec{k}}$, $R_{*\text{tot}}\lambda_{\vec{k}}F(\mathcal{R}_b \circ \mathcal{P}_b \circ \mathcal{S}_{\vec{k}}(|\mathbf{0}\rangle\langle\mathbf{0}|))$ is an unbiased estimator for p_L . Since it is bounded in a range $[-R_{*\text{tot}}, R_{*\text{tot}}]$, from Hoeffding inequality [23], the number of samples M needed to estimate p_L within additive error ϵ with probability at least $1 - \delta$ is given by

$$M = \frac{2}{\epsilon^2} R_{*\text{tot}}^2 \ln\left(\frac{2}{\delta}\right).$$
 (5)

Note that, when we only consider Clifford noise, $\frac{2}{\epsilon^2} \ln\left(\frac{2}{\delta}\right)$ samples suffice to achieve the same accuracy. Therefore, $R^2_{*\text{tot}}$ quantifies the additional overhead required for including the effect of non-Clifford channels.

3. Planar surface codes under coherent noise

To demonstrate the feasibility of the proposed method,



I Layout of d = 3 planar surface code. The white and black circles represent data and measurement qubits, respectively. The light blue square and triangular patches show X stabilizers. The red square and triangular patches show Z stabilizers.

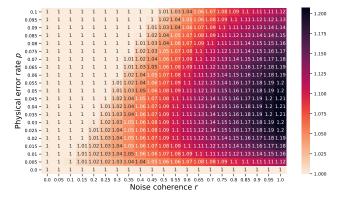
we consider the planar surface code introduced in Ref. [24], which is thought to be one of the most promising candidates for an experimental realization of QEC, as they require only single- and nearest-neighbor two-qubit gates on two-dimensional arrays of qubits [8], [25]. The planar surface code with code distance d has a $(2d-1) \times (2d-1)$ square grid of qubits of which $d^2 + (d-1)^2$ data qubits are used to encode the logical qubit and 2d(d-1) measurement qubits are used to extract the syndromes. In Fig. 1, we show the layout of d = 3 planar surface code as an example.

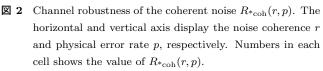
In numerical simulations, the ideal logical state $|0\rangle_L$ followed by single-qubit noise is prepared as the initial state. We assume two types of noise model: code-capacity noise model, where the noise occurs in all data qubits with perfect syndrome measurements, and phenomenological noise model, where the noise occurs in all data qubits and measurement qubits just before the syndrome measurements. The number of rounds of the syndrome measurement in the latter case is d. We also assume that the syndrome measurements are performed perfectly at the final cycle. In both cases, Z-type and X-type errors are uncorrelated, and hence only X-type errors and syndrome measurements are simulated for simplicity. The specific noise channel \mathcal{N}_{coh} simulated in this work is a mixture of coherent and incoherent noise which is modeled by the over-rotation noise followed by the bit-flip X error as,

$$\mathcal{N}_{\text{coh}} \coloneqq \mathcal{N}_{\text{bit-flip}} \circ \mathcal{N}_{\text{over-rot}},$$
$$\mathcal{N}_{\text{over-rot}} \coloneqq [e^{ir\theta X}],$$
$$\mathcal{N}_{\text{bit-flip}} \coloneqq (1-p)[I] + p[X],$$

where [A] is a superoperator defined as $[A]\rho := A\rho A^{\dagger}$, and θ is chosen such that $p = \sin^2 \theta$. We vary the parameters (r, p) and evaluate the performance of the code by using the method described in Sec. 2.

We first examine the sampling cost of our simula-





tion which is characterized by the channel robustness $R_{*coh}(r,p)$ of \mathcal{N}_{coh} [15], [19], [20]. The CSP channels employed to decompose \mathcal{N}_{coh} are $[I], [X], [e^{-i(\pi/4)X}]$, and $[Xe^{-i(\pi/4)X}]$. Figure 2 shows the values of $R_{*coh}(r,p)$. From Fig. 2, we confirm that the channel robustness increases as the noise coherence becomes larger as expected. Importantly, for a small r with a sufficiently large p, the channel robustness decreases and hits 1.0, where an efficient simulation of coherent errors can be performed. This is because $r\theta$ is small in this region, resulting in the low channel robustness of $\mathcal{N}_{over-rot}$. Thus, the bit-flip noise with large p can easily make the channel robustness 1.0. On the other hand, at p = 0, the channel robustness of $\mathcal{N}_{\text{over-rot}}$ is unity even if r > 0 since we set $p = \sin^2 \theta$. Therefore certain mixture of incoherent and coherent errors, for example, with p = 1% (0.1%) and r = 0.10(r = 0.46), can be efficiently simulated without any additional overhead, which is in an experimentally important parameter region. This greatly improves the simulation cost over Ref. [16] which is a result of decomposing $\mathcal{N}_{\rm coh}$ as a whole rather than decomposing $\mathcal{N}_{over-rot}$ and $\mathcal{N}_{bit-flip}$ individually.

The number of samples needed for accurate results is determined by R^2_{*tot} via Eq. (5). In the case of the code capacity noise, \mathcal{N}_{coh} is applied for $d^2 + (d-1)^2$ times, which corresponds to the number of data qubits. Therefore, $R^2_{*tot} = (R_{*coh}(r,p))^{2(d^2+(d-1)^2)}$. In the case of the phenomenological noise, \mathcal{N}_{coh} is applied to each of $d^2 + (d-1)^2$ data qubits for d times and $d(d-1)^2$ measurement qubits for X-type errors for d-1 times since we assume perfect measurement in the final round. Overall, \mathcal{N}_{coh} is applied for $d(3d^2 - 4d + 2)$ times, which means $R^2_{*tot} = (R_{*coh}(r,p))^{2d(3d^2-4d+2)}$ in this case. These for-

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3 × 10⁻

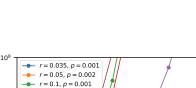
 2×10^{-1}

10-

8.5 >

Logical X error rate P_L

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r = 0.1, p = 0.005r = 0.1, p = 0.008

= 0.15, p = 0.015

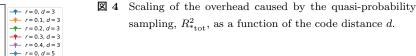
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r = 0.6, d =

19

17



with faulty syndrome measurements to the best of our knowledge.

11

distance

13

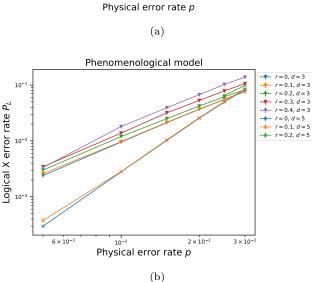
d

15

Finally, let us discuss with which parameters and code distance the proposed method works. Figure 4 shows the dependence of $R^2_{\text{*tot}}$ with respect to d for the phenomenological noise model. Note that for the parameters where $R_{\text{*coh}}(r, p) = 1$ in Fig. 2, we can simulate without any additional overhead as mentioned before. We will be able to simulate large code distances in that region. Outside of that, we expect regions with $R_{\text{*tot}} < 10^3$ are within reach if a high-performance parallel computer of 10^6 CPU cores is available. For example, realistic parameters such as (p, r, d) = (1.5%, 0.15%, 7) and = (0.2%, 0.05, 13) result in $R_{\text{*tot}} < 10^3$. Full state-vector simulation would not work for these numbers of qubits; we need 169 qubits for d = 7 and 625 qubits for d = 13.

4. Conclusion

We have proposed the sampling-based method to estimate the logical error rate of QEC codes under coherent noise such as an over-rotation error. The simulation protocol is based on the quasi-probability decomposition of noise channels into Clifford operations. It is interesting to note that the QEC process is simulated as usual for sampled CSP channels, and hence the probability distribution for the syndrome measurements is far different from the true one p(b). However, if we sample whether the decoding successes or fails with the quasi-probability method we can estimate the logical error rate. By calculating the channel robustness for the mixture of coherent and incoherent errors, we reduce the simulation costs substantially, which allows us to simulate a practically important parameter



Code capacity model

 9.5×10^{-1}

10

1.05 × 10-1

1.1 × 10

☑ 3 Logical X error rate of the planar surface code under the coherent noise as a function of the physical error rate p and noise coherence r in the case of code capacity (a) and phenomenological (b) noise. The horizontal axis shows the physical error rate p, and the vertical axis shows the logical X error rate p_L . The triangles, crosses, and circles stand for d = 3, 5, 7 respectively. The color shows the noise coherence.

mulae for R_{*tot}^2 provides us estimates of simulation cost for a given (p, r, d), based on which we choose the parameter range investigated below.

Figure 3 shows the logical error rate p_L as a function of physical error rate p and noise coherence r, where the parameters are chosen such that our workstation with Intel Xeon CPU v4 CPU (E5-2687W), 24 cores, 3.00GHz, can calculate each point within a few days at most. We confirmed that the standard error of each data point is below 10^{-3} . From Fig. 3, the logical error rate increases as the noise coherence grows, which implies that the impact of the coherent noise on the logical error probability is not negligible even for a relatively large code distance. Note that the d = 5 code, which requires 81 qubits, is well beyond the reach of naive full state-vector simulation. Furthermore, it is the first analysis of this region region with a relatively large code distance without any additional overhead or with a reasonable additional overhead. While we have only considered the phenomenological noise model, it is straightforward to extend our method to the circuit-level noise model, where each elementary gate is followed by noise. We leave these problems for future works. We believe that this work helps to analyze the performance of the near-term small-scale QEC in realistic situations.

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