Welcome to TSLDM — A New Open-Access Online Journal from IPSJ

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On behalf of the editorial board, I am pleased to present the new IPSJ Transactions on System LSI Design Methodology (TSLDM).

The rapid progress in manufacturing technology development combined with the expanding diversity of information processing applications is placing enormous demands on the high performance, yet affordable design and automation of integrated circuits and systems. As we are going into the era of nano-scale integrated circuits, more than billions of transistors can be packed onto a single chip, resulting in the integration of the whole electronic system as a System LSI (System-on-Chip). At the same time, many issues are confronting us such as complexity, manufacturability, and power dissipation. These issues can be overcome by cross-disciplinary researches with tighter collaboration between manufacturing, design, and application technologies, as well as cutting-edge researches that push the limits of current design methodologies. TSLDM provides the best opportunity for all researchers working on System LSI Design Methodologies to publish and learn state-of-the-art achievements in the various fields.

Keeping pace with the breath-taking speed of technology advancement, TSLDM will provide a timely and rapid publication. Every issue has a predetermined editorial schedule announced in the call for papers. Authors can therefore expect the date of publication to be just nine months after the submission is due. This makes it more attractive to submit and publish up-to-date information on research and development. We hope that TSLDM offers an ideal forum for an exchange of ideas about the challenges and solutions for the future.

Finally, we would like to extend our special thanks to the IPSJ Special Interest

Group on System LSI Design Methodology which initiated the effort of launching TSLDM and continues to provide various kinds of support including finance.

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